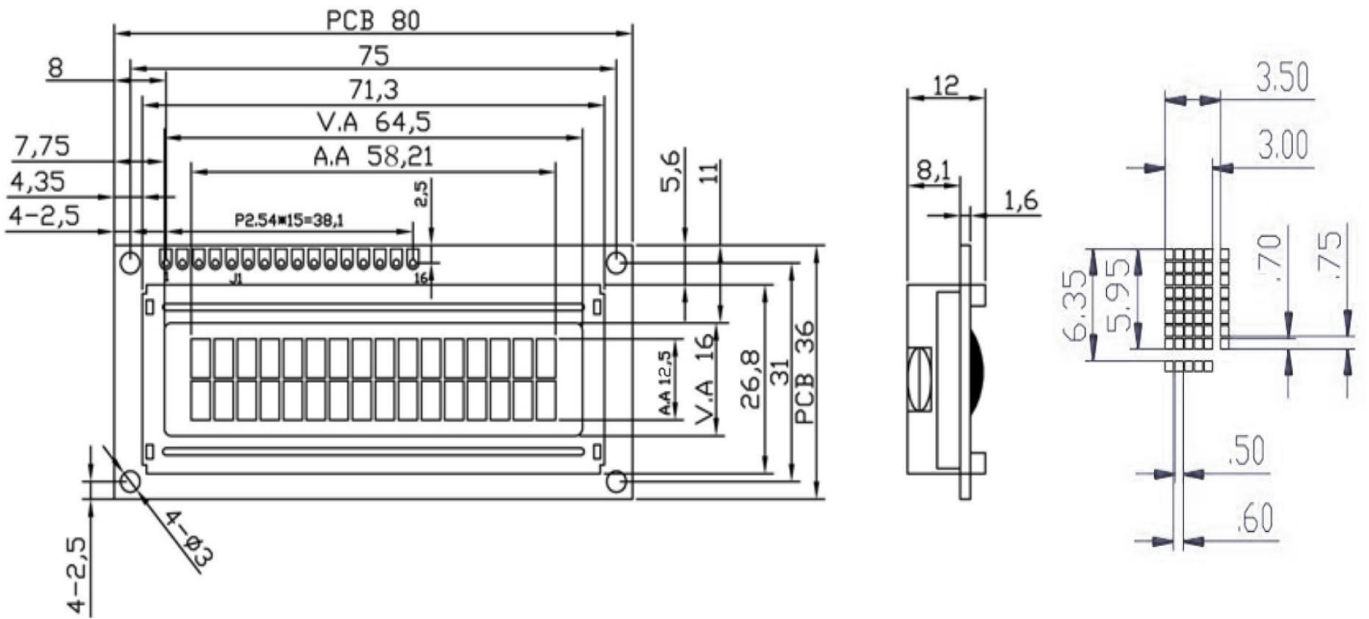


**PHYSICAL DIMENSIONS**



**FUNCTIONS & FEATURES**

Characters: 16x2 Lines pure green  
 LCD Mode: DFSTN negative transmissive  
 Controller IC: SPLC780D or Equivalent  
 Viewing Angle: 6 O'clock direction  
 6800 serial 8-Bit/4-Bit MPU Interface  
 Backlight: LED white 1-LED 15mA 520[nm]  
 Operating Temperature Range: -20° to +70°  
 Storage Temperature Range : -30° to +80°

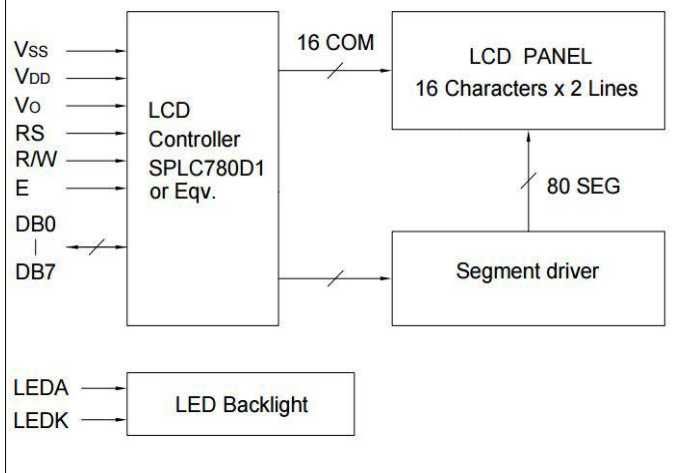
**MECHANICAL DATA**

Module size with backlight	80x36x12	[mm]
View Area	64x16	[mm]
Active Area	58,2x12,5	[mm]

**PIN CONFIGURATION**

1	GND	POWER GROUND
2	VDD	POWER POSITIVE 5V
3	Vo	OP.VOLTAGE FOR LCD
4	R/S	H:DATA L:INSTRUCTION
5	R/W	H:READ L:WRITE
6	E	ENABLE TRIGGER
7	DB0	DATA BUS LSB
8	DB1	DATA BUS
9	DB2	DATA BUS
10	DB3	DATA BUS
11	DB4	DATA BUS
12	DB5	DATA BUS
13	DB6	DATA BUS
14	DB7	DATA BUS MSB
15	LED +	BACKLIGHT +5V
16	LED -	BACKLIGHT 0V

**BLOCK DIAGRAM**



**ABSOLUTE MAXIMUM RATINGS (25°)**

SUPPLY VOLTAGE LOGIC	MAX. 5.5	[V]
INPUT VOLTAGE	VDD	[V]

CHARACTER TABLE

Upper 4 bit Lower 4 bit	LLLL	LLLH	LLHL	LLHH	LHLL	LHLH	LHHL	LHHH	HLLL	HLLH	HLHL	HLHH	HHLL	HHLH	HHHL	HHHH
LLLL				0	1	2	3	4				一	夕	三	四	五
LLLH			!	1	A	Q	3	4			。	ア	イ	ウ	エ	オ
LLHL			"	2	B	R	b	n			「	イ	ウ	※	目	目
LLHH			#	3	C	S	c	s			」	ウ	ア	目	三	※
LHLL			4	4	O	T	t	o			、	エ	ト	カ	目	目
LHLH			5	5	E	U	e	u			・	オ	太	工	目	目
LHHL			6	6	F	V	f	v			ヲ	カ	ニ	目	目	目
LHHH			7	7	G	W	g	w			ヲ	キ	ヌ	ヲ	目	目
HLLL			8	8	H	X	h	x			、	ウ	ホ	目	目	目
HLLH			9	9	I	Y	i	y			ウ	テ	目	目	目	目
HLHL			*	*	J	Z	j	z			三	目	目	目	目	目
HLHH			+	+	K	C	k	c			ホ	カ	目	目	目	目
HHLL			,	,	L	X	l	x			ホ	目	目	目	目	目
HHLH			-	-	M	N	m	n			ユ	ヌ	ホ	目	目	目
HHHL			.	.	N	^	n	^			ヨ	セ	ホ	目	目	目
HHHH			/	/	O	0	o	0			ウ	ウ	ヌ	目	目	■

## INSTRUCTION TABLE

Instruction	Instruction Code										Description	Execution time (fosc=270KHz)	
	RS	RW	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0			
Clear Display	0	0	0	0	0	0	0	0	0	1	Write "20H" to DDRAM and set DDRAM address to "00H" from AC	1.52ms	
Return Home	0	0	0	0	0	0	0	0	0	1	- Set DDRAM address to "00H" from AC and return cursor to its original position if shifted. The contents of DDRAM are not changed.	1.52ms	
Entry Mode Set	0	0	0	0	0	0	0	0	1	I/D	S	Assign cursor moving direction and enable the shift of entire display	38 $\mu$ s
Display ON/OFF Control	0	0	0	0	0	0	0	1	D	C	B	Set display(D), cursor(C), and blinking of cursor(B) on/off control bit.	38 $\mu$ s
Cursor or Display Shift	0	0	0	0	0	0	1	S/C	R/L	-	-	Set cursor moving and display shift control bit, and the direction, without changing of DDRAM data.	38 $\mu$ s
Function Set	0	0	0	0	0	1	DL	N	F	-	-	Set interface data length (DL: 8-bit/4-bit), numbers of display line (N: 2-line/1-line) and, display font type (F:5x10 dots/5x8 dots)	38 $\mu$ s
Set CGRAM Address	0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0	Set CGRAM address in address counter.	38 $\mu$ s	
Set DDRAM Address	0	0	1	AC6	AC5	AC4	AC3	AC2	AC1	AC0	Set DDRAM address in counter	38 $\mu$ s	
Read Busy Flag and Address Counter	0	1	BF	AC6	AC5	AC4	AC3	AC2	AC1	AC0	Whether during internal operation or not can be known by reading BF. The contents of address counter can also be read.		
Write Data to RAM	1	0	D7	D6	D5	D4	D3	D2	D1	D0	Write data into internal RAM (DDRAM/CGRAM).	38 $\mu$ s	
Read Data from RAM	1	1	D7	D6	D5	D4	D3	D2	D1	D0	Read data from internal RAM (DDRAM/CGRAM).	38 $\mu$ s	